

Notice of References Cited	Application/Control No. 10/708,796	Applicant(s)/Patent Under Reexamination CHUANG ET AL.	
	Examiner Hong C. Kim	Art Unit 2185	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,484,270	11-2002	Odani, Nobutsugu	714/6
*	B	US-6,496,411	12-2002	Yamada et al.	365/185.03
*	C	US-6,906,954	06-2005	Shukuri et al.	365/185.05
*	D	US-2002/0013879	01-2002	HAN, SANG-WOOK	711/103
*	E	US-2004/0078513	04-2004	Yamazaki, Satoshi	711/104
*	F	US-2004/0264234	12-2004	Moore et al.	365/148
*	G	US-2005/0055488	03-2005	Chuang et al.	710/308
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Intel 815 Chipset family: 82815 Graphics and Memory Controller Hub (GMCH) Jan 2003, Fig. 1 pp 11 and 15.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.